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Applicant : Kenneth C. Creta et al.

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Assignee : Intel Corporation

Examiner : Midys Inoa

Serial No. : 10/035,034

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Title : CACHE MEMORY EVICTION POLICY FOR COMBINING WRITE
TRANSACTIONS

OFFICIAL

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Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

REPLY OF INTEL CORPORATION TO ACTION OF FEBRUARY 11, 2004

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